

ABSTRACT

An interpolation circuit capable of performing interpolation operation with a simple constitution. A 16-times oversampling from discrete data is performed by D flip-flops 4, 5. A first convolution operation is performed by D flip-flops 4 through 11 and an adder 12, on the result of which a second convolution operation is performed by D flip-flops 13 through 20 and an adder 21. Data interpolated along a quadratic function curve interpolating the discrete data is obtained from the adder 21.

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